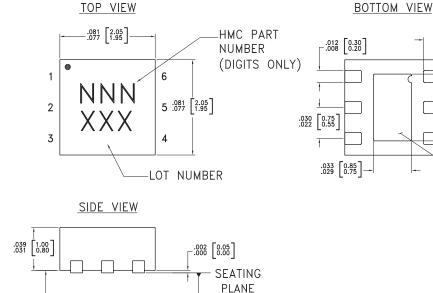


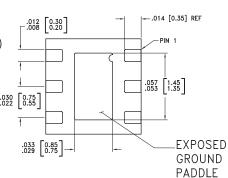
# LP2 (E) – 2 x 2 mm DUAL FLATPACK NO-LEAD (DFN) PLASTIC PACKAGE

# LP2 (E) Package Outline Drawing

\_\_\_\_.003[0.08] C



-C-



### NOTES:

- 1. LEADFRAME MATERIAL: COPPER ALLOY
- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
- 3. PAD SPACING TOLERANCE IS NON-CUMULATIVE
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- 6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

## Package Information

Part Number Suffix	Package Body Material	Lead Finish	MSL Rating	Package Marking [3][4]
LP2	RoHS Compliant Mold Compound	Sn/Pb Solder	MSL1 <sup>[1]</sup>	NNN XXX
LP2E	RoHS Compliant Mold Compound	100% matte Sn	MSL1 <sup>[2]</sup>	NNN XXX

[1] Max peak reflow temperature of 235 °C

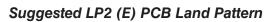
[2] Max peak reflow temperature of 260 °C [3] 4-Digit lot number XXXX

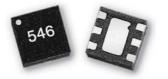
[4] 3-Digit part number NNN

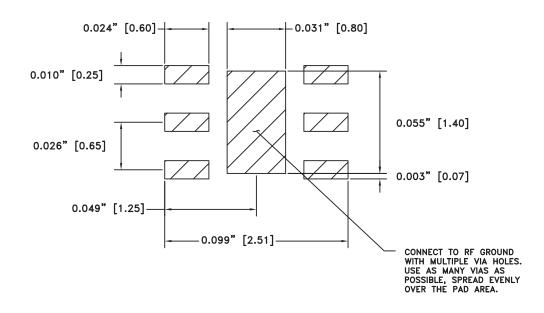


# LP2 (E) – 2 x 2 mm DUAL FLATPACK NO-LEAD (DFN) PLASTIC PACKAGE

PACKAGE OUTLINES







## NOTES:

- 1. DIMENSIONS ARE IN INCHES [MILLIMETERS].
- 2. PAD WIDTH SHOWN IS FOR SOLDERING ONLY. BEYOND SOLDERING AREA ALL CONDUCTORS THAT CARRY RF AND MICROWAVE SIGNALS SHOULD HAVE 50 OHM CHARACTERISTIC IMPEDANCE.
- 3. SOLDERMASK ON FAR SIDE SHOULD TENT OR PLUG VIA HOLES.